

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Javendar Rajagopalan, et al.

U.S. Serial No.

10/654,318

Filed

September 3, 2003

For

DISCRETE-TIME **AMPLITUDE** CONTROL **VOLTAGE-**OF

CONTROLLED OSCILLATOR

Group No.

2817

Examiner

(Not Yet Assigned)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

Sir:

The undersigned hereby certifies that the following documents:

- 1. Information Disclosure Statement:
- 2. Form PTO/SB/08B;
- Two (2) references as cited on Information Disclosure Statement; and 3.
- 4. A postcard receipt;

relating to the above application, were deposited as "First Class Mail" with the United States Postal Service, addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on 7.1.2, 200.4

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Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO/SB/08. Copies of the listed references are submitted herewith.

DOCKET NO. P05655 (NATI15-05655) U.S. SERIAL No. 10/654,318

PATENT

Publications

MARGARIT, Mihai A., et al, "A Low-Noise, Low-Power VCO With Automatic Amplitude Control for Wireless Applications," IEEE Journal of Solid-State Circuits, Vol. 34, No. 6, June 1999.

pp. 761-771.

ROGERS, John W.M., et al., "A Study of Digital and Analog Automatic-Amplitude Control Circuitry for Voltage-Controlled Oscillators," IEEE Journal of Solid-State Circuits, Vol. 38, No. 2,

February 2003, pp. 352-356.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of

the above Patents and to question the relevance and materiality of the Patents and Publications listed

herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure

Statement.

Respectfully submitted,

DAVIS MUNCK, P.C.

Date: Jan. 30, 2004

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PTO/SB/08B (08-03)

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|) ogskindn | 3 101 101111 1443/7 10 | | | Application Number | 10/654,318 | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) | | | | Filing Date | September 3, 2003 | |
| | | | | First Named Inventor | Javendar Rajagopalan | |
| | | | | Art Unit | 2817 | |
| | | | | Examiner Name | (Not Yet Assigned) | |
| Sheet | 1 | of | 11 | Attorney Docket Number | P05655 | |

THO HE WELL

| Examiner | Cite | NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of | |
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| Initials* | No. ¹ | the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T² |
| | AA | MARGARIT, Mihai A., et al, "A Low-Noise, Low-Power VCO With Automatic Amplitude Control for Wireless Applications," IEEE Journal of Solid-State Circuits, Vol. 34, No. 6, June 1999, pp. 761-771. | |
| | АВ | ROGERS, John W.M., et al., "A Study of Digital and Analog Automatic-Amplitude Control Circuitry for Voltage-Controlled Oscillators," IEEE Journal of Solid-State Circuits, Vol. 38, No. 2, February 2003, pp. 352-356. | |
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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